

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-26. (Canceled)

27. (Previously Presented) A display device, comprising:

a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;

a scanning line driver that drives the plurality of scanning lines;

a data line driver that drives the plurality of data lines;

a memory cell section having a plurality of memory cells, the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, the memory cell section being disposed between the display section and the selection switch, and the display section, the memory cell section, and the selection switch section being formed on one substrate.

28. (Previously Presented) The display device according to claim 27, the memory cell section storing image signals for one screen.

29. (Currently Amended) The display device according to claim 27, further ~~comprising a~~ comprising:

a plurality of word lines among which a word line is selected when a memory cell of the plurality of memory cells receives the image signal,

the plurality of word lines that extend~~extending~~ along a row direction that intersects a column direction along which the plurality of data lines extend.

30. (Previously Presented) The display device according to claim 27, the number of the plurality of rows being equal to the number of the plurality of scanning lines.

31. (Currently Amended) The display device according to ~~claim 29~~claim 27, further comprising a word line driver that drives the plurality of word lines, ~~a plurality of memory cells that are disposed in the row direction and that are connected to a word line selected by the word line driver among the plurality of memory cells receiving image signals.~~

32. (Currently Amended) The display device according to claim 27, further comprising a DAC section having a plurality of DACs,
each of the plurality of DACs receiving digital data based on the image signal,
the DAC

each of the plurality of DACs converting the digital data into analog data, ~~the DAC being disposed between the memory cell section and the display section, the DAC being disposed on the one substrate together with the display section, the memory cell section, and the selection switch section.~~

33. (Previously Presented) The display device according to claim 27, each of the plurality of memory cells of the memory cell section being configured by any one of a dynamic memory and a static memory.

34. (Previously Presented) The display device according to claim 27, each of the memory cell of the memory cell section rewriting data based on the image signal without refreshing.

35. (Currently Amended) The display device according to claim 27, ~~the DAC converting data for~~ only when a change of display in one dot of the plurality of dots ~~that is~~

~~not to be rewritten as it is, the data being stored in~~ is carried out, a memory cell of the plurality of memory cells that corresponds to the one dot receiving the image signal.

36. (Currently Amended) The display device according to claim 27, the memory cell section ~~functioning acting~~ as a frame memory.

37. (Currently Amended) A display device, comprising:
a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;
a scanning line driver that drives the plurality of scanning lines;
a data line driver that drives the plurality of data lines;
a memory cell section having a plurality of memory cells, ~~the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns,~~ a length of the ~~matrix~~ memory cell section in a row direction that intersects a column direction along which the plurality of data lines extend being shorter than a length of the display section in the row direction; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, ~~the memory cell section being disposed between the display section and the selection switch,~~ and the display section, the memory cell section, and the selection switch section being formed on one substrate.

38-44. (Canceled)

45. (New) The display device according to claim 31, the word line driver receiving an address signal that is received by the scanning line driver.

46. (New) The display device according to claim 31, further comprising a column decoder that selects a memory cell of the plurality of memory cells by cooperation with the word line.

47. (New) A display device, comprising:
a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;
a scanning line driver that drives the plurality of scanning lines;
a data line driver that drives the plurality of data lines; and
a memory cell section having a plurality of memory cells, the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns,
each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, and
the display section and the memory cell section being formed on one substrate.

48. (New) The display according to claim 47,
a length of the memory cell section in a row direction that intersects a column direction along which the plurality of data lines extend being shorter than a length of the display section in the row direction.

49. (New) The display device according to claim 47, the number of the plurality of rows of the memory cell section being more than two.

50. (New) The display device according to claim 47, the memory cell section storing image for one screen.

51. (New) The display device according to claim 47, the number of the plurality of rows of the memory cell section being equal to the number of the plurality of scanning lines.

52. (New) The display device according to claim 47, the memory cell section acting as a frame memory.

53. (New) The display device according to claim 47, a gray-scale level in the display section being obtained by at least one of an area gray-scale method, time-division gray-scale method and a method combining the area gray-scale method and the time-division gray-scale method.